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In computer engineering, microarchitecture, also called computer organization and sometimes abbreviated as µarch or uarch, is the way a given instruction set architecture (ISA), is implemented in a particular processor. A given ISA may be implemented with different microarchitectures; implementations may vary due to different goals of a given design or due to shifts in technology.

Microarchitecture - Wikipedia

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COURSE STRUCTURE FOR MCA 1 FIRST SEMESTER A. THEORY CODE THEORY CONTACTS (PERIODS/WEEK) SL. NO. L T P TOTAL CREDITS 1 MCA101 Computer Organisation & Architecture 3 1 - 4 4

FIRST SEMESTER A. THEORY

A complex instruction set computer (CISC / Ę s Ę s k /) is a computer in which single instructions can execute several low-level operations (such as a load from memory, an arithmetic operation, and a memory store) or are capable of multi-step operations or addressing modes within single instructions. The term was retroactively coined in contrast to reduced instruction set computer (RISC) and ...

Complex instruction set computer - Wikipedia

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This index mentions most of the computer pioneers and their inventions, or other important people in computers or computing industry.

Biographies of computer pioneers A-J

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Esta obra explica la arquitectura, organizaci3n y dise±o de computadoras, y analiza la relaci3n del lenguaje de transferencia de registros con la organizaci3n de la circuiter-a y el dise±o de equipos digitales. Cada cap-tulo incluye un conjunto de problemas y una lista de referencias. En esta edici3n se incluye un cap-tulo acerca del procesamiento por arquitectura paralela y vectores ...

Arquitectura de Computadoras - M. Morris Mano - 3ra Edici3n

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